

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

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: Examiner: Aristotelis M. Psitos

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Title: PLL CLOCK GENERATOR, OPTICAL DISC DRIVE AND METHOD FOR
CONTROLLING PLL CLOCK GENERATOR

AMENDMENT AFTER FINAL REJECTION UNDER 37 C.F.R. § 1.116

This paper is being filed in response to the Office Action dated June 4, 2007. This paper is being filed within the shortened statutory period set for a response to the Office Action. No fee is believed to be due for filing this Amendment. However, if any fee is due, charge the fee to Deposit Account No. 50-1017 (Billing No. 210407.0086).

Please amend the application, without prejudice, as follows:

Amendments to and Listing of the Claims begin on page 2 of this paper.

Remarks and Conclusion begin on page 7 of this paper.